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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lin, et al. Docket No.: TSM03-0670
Filed: 12/5/2003 Examiner: Matthew E. Warren
Serial No.: 10/729,092 Art Unit: 2815
Title: Structure and Method of Forming Integrated Circuits Utilizing Strained Channel Transistors

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

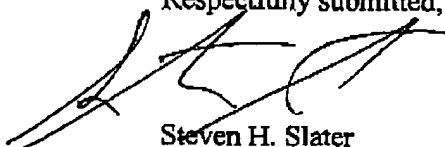
Dear Sir:

Applicants wish to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08a, which may be considered material to the examination of the above-identified application.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment, to Deposit Acct. No 50-1065 of the below mentioned firm.

Respectfully submitted,



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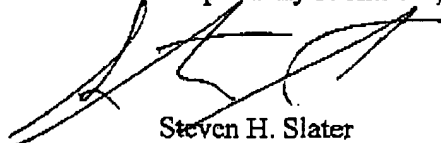
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*EXAMINER: Initial if reference considered, whether or not citation is in conflict with this report.

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